



Minutes of the Meeting
VASAVI COLLEGE OF ENGINEERING
(Autonomous)

9-5-81, Ibrahimbagh, Hyderabad-500031, Telangana, India

(Sponsored by Vasavi Academy of Education)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 27.12.2014

Minutes of the meeting of Board of Studies, Electronics and Communication Engineering department, held at 11.00 AM on 27.12.2014 (Saturday) at the Department of Electronics and Communication Engineering, Vasavi College of Engineering, Ibrahimbagh, Hyderabad.

Members Present:

- | | | |
|-----|----------------------------|------------------|
| 1. | Dr. K. Jaya Sankar | Prof. & Head |
| 2. | Prof. D. Srinivasa Rao, | Professor |
| 3. | Mr. Raghu Prasanth, | Director |
| 4. | Dr. P.A. Govindacharyulu | Professor |
| 5. | Dr. M. Satyam | Professor |
| 6. | Prof. R.B. Rajendra Prasad | Professor |
| 7. | Prof. P. Venkata Ramana | Professor |
| 8. | Dr. E. Sreenivasa Rao | Professor |
| 9. | Ms. G.R. Padmini | Assoc. Professor |
| 10. | Dr. N. Siva Sankar Reddy | Assoc. Professor |
| 11. | Mr. G. Venkateswarlu, | Assoc. Professor |
| 12. | Mrs. Vibha D.Kulkarni | Asst. Professor |
| 13. | Mrs. K. Deepthi | Asst. Professor |
| 14. | Mr. N. Abid Ali Khan | Asst. Professor |
| 15. | Mr. R. Goutham | Asst. Professor |
| 16. | B. Uma Mahesh Babu | Asst. Professor |

The following members of Board of Studies could not attend the meeting

1. Prof. S. Varada Rajan, S.V. University, Tirupathi.
2. Prof. Sasi Bhushan Rao, Andhra University, Visakhapatnam.
3. Mr. Vemuri Hanuma Sai, AMS, Hyderabad.
4. Mrs. A. Sri Lakshmi, Assoc. Prof., Vasavi College of Engineering.

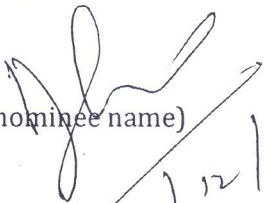
Chairman welcomed the members to the meeting.


The following have been resolved in the meeting.

1. B.E 1/4 - ECE Workshop Practice - II, Syllabus proposed under autonomy has been discussed and approved.
2. Curriculum related to PG Programs has been discussed and it is decided to:
 - (i) Rename *Real Time Operating* subject as **Embedded Real Time Operating Systems** and to be moved to list of Core subjects.
 - (ii) After introducing ESD topic in *Principles of VLSI System Design* subject to be moved to list of Elective subjects.

- (iii) One unit syllabus content on SIP to be introduced in the subject *System on Chip Architecture*.
- (iv) In the subject *Digital IC Design*, Verilog Design only to be covered. One unit syllabus content pertaining to Design of Synchronous and Asynchronous Systems and their Minimization to be introduced.
- (v) In the list of Elective subjects, Design of Fault Tolerant Systems, Hardware and Software Co-Design are to be included.
- (vi) One unit syllabus content pertaining to Low Frequency Design for Medical Devices is to be included in the subject Low Power VLSI Design.
- (vii) Rename Voice and Speech Processing subject as **Speech Signal Processing**.
- (viii) For CE & SP Specialization, *Embedded Systems Application Laboratory* is to be renamed as **DSP Processors Application Laboratory**.
- (ix) For CE & SP Specialization, a subject on CODECS is to be included and experiments related to CODECS are to be introduced in DSP Processors Application Laboratory.
- (x) All the laboratory courses in PG programs are required to focus on **Mini Project** at the end of the Semester.

Meeting concluded with vote of thanks.


(OU nominee name)
27/12/14



(Chairman) 27/12/14


Dr. K. JAYA SANKAR

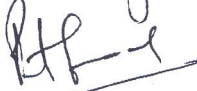
M. Senthyan


E. Sreenivasa Rao



(N. S. Reddy)



(G. VENKATESWARLU)


(R. Raghu Prashanth)

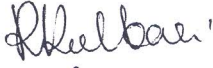

(P. A. Govindaraj)

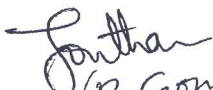

(K. DEEPTHI)

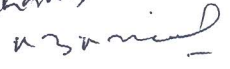

(G. R. PADMINI)

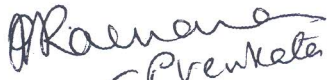

(G. R. PADMINI)



W. ABDUL WAHID


(Vibha Kulkarni)


(R. Goutham)


(R. B. R. PRASAD)


(P. Venkatesh)


(B. V. MAHAESH)