

Paper ID	Title
Pp-32	Performance analysis of peak to average power Ratio reduction in ofdm using companding
Pp-14	Effectiveness of ofdm as a modulation technique for wireless radio application
Pp-23	Single snapshot doa estimation using 3 d array
Pp-13	Quadrature phase shift keying modulator & demodulator for wireless modem
Pp-12	Microwave radar angle-of-arrival
Pp-10	Improving atm security via face recognition
Pp-9	High density fpga based waveform generation for radars
Pp-4	Methods of return loss improvement of rectangular Microstrip antenna
Pp-25	Reducing mutual coupling effects in arrays
Pp-34	Study and development of dual slot microstrip antenna and its characteristics
Pp-5	Analysis and comparison of formulation techniques for fault tolerance in sensor networks
Pp-8	Efficient detection for mimo transmission using qr-osisc receiver
Pp-15	A location based distributed database architecture for global roaming in next generation mobile networks
Pp-20	CMOS Low VOLTAGE LNA for GPS L1 Signal
Pp-3	A HIGH-SPEED LOW-POWER MODIFIED ERROR TOLERANT ADDER
Pp-28	On Reducing Scan Shift Activity at RTL
Pp-17	IMPLEMENTATION OF ENCRYPTION/ DECRYPTION FOR THE SAFER+ ALGORITHM USING VERILOG HDL
Pp-16	Automatic Railway Gate Control Using 89C51 Microcontroller